

Description

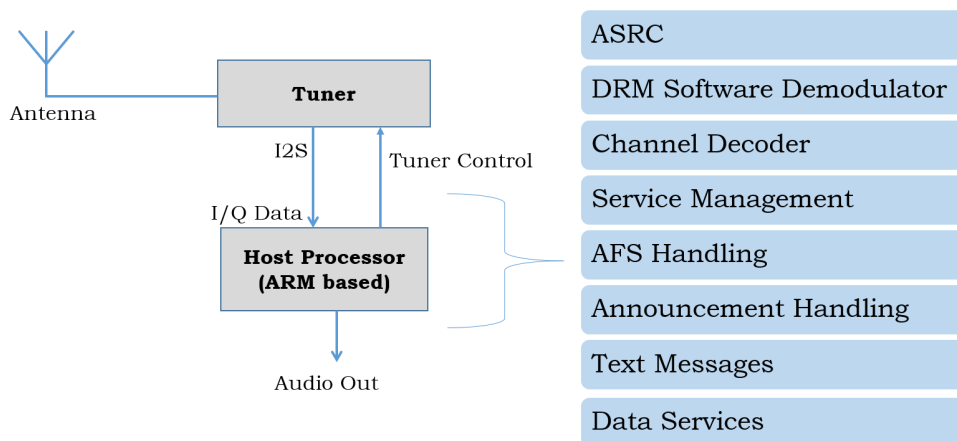
- SDR implementation on generic ARM processor
- Highly Optimized for ARM
- ASRC component for sample rate conversion
- Data Services common with DAB - Journaline, MOT Decoder, MOT Slideshow
- Compliant with DRM Minimum Receiver Requirements for MW
- Field Tested in Indian Cities
- Portable across Platforms/SoC/Operating System
- Cost Effective

Applications

- Automotive Infotainment
- Consumer Devices
- USB Dongle based devices

Standards

- DRM System Specification - ETSI ES 201 980 V4.1.2
- DRM Minimum Receiver Requirements v4.0
- DAB Journaline - ETSI TS 102 979 V1.1.1
- DAB MOT Protocol - ETSI EN 301 234 V2.1.1
- DAB MOT Slideshow - ETSI TS 101 499 V2.2.1



Features

- Supports DRM30 (Echo30) and DRM+ (Echo+)
- Manual Scan
- Auto Scan
- Service playback
- Service List
- Service Details
- Text Message
- Tuning from DRM component.
- AFS Signaling
- Announcements including Emergency Warning
- Reconfiguration
- Channel Seek Up/Down
- Service Seek Up/Down
- Manual Channel Up/Down
- DRM Data services
- DRM Packet Decoding

Demodulator & Channel Decoder

- FAC - 4QAM
- SDC - 4QAM, 16QAM
- MSC - 16QAM, 64QAM SM, 64QAM HMsym, 64QAM HMmix
- MSC - Multiple combinations of protection levels, short and long interleaving
- Robustness Mode A: Spectrum Occupancy 4.5kHz, 5kHz, 9kHz, 10kHz, 18kHz, 20kHz
- Robustness Mode B: Spectrum Occupancy 4.5kHz, 5kHz, 9kHz, 10kHz, 18kHz, 20kHz
- Robustness Mode C: Spectrum Occupancy 10kHz, 20kHz
- Robustness Mode D: Spectrum Occupancy 10kHz, 20kHz
- Robustness Mode E: Spectrum Occupancy 100kHz
- BER, PC BER Computation